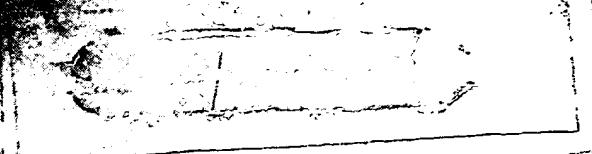




ISSUE CLASSIFICATION						PATENT NUMBER
Class	Subclass					
						
<b>U.S. UTILITY Patent Application</b>						
O.I.P.E.				PATENT DATE		
 SCANNED				 Q.A. 		
APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER	
09/827915	F	375	101	221	HIB A. HAGIWARA	
TITLE APPLICANTS						
Kazuyuki Ichimura Mitsuo Suzuki						
Variable bit rate digital circuit multiplying function with passthrough function						
PTO-840 12/00						

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				<b>ISSUE FEE</b>	
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